FIG.1A

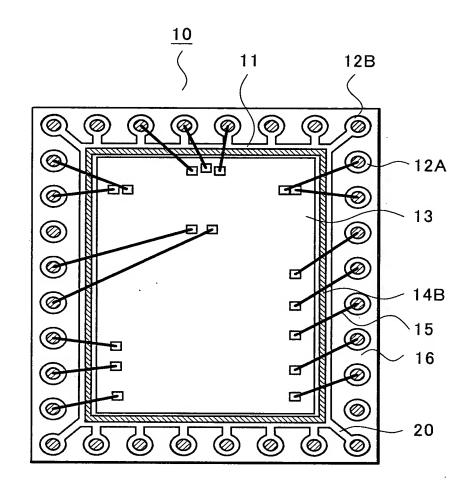


FIG.1B

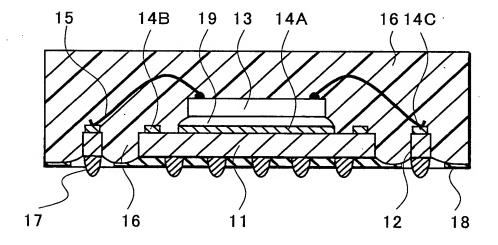


FIG.2A

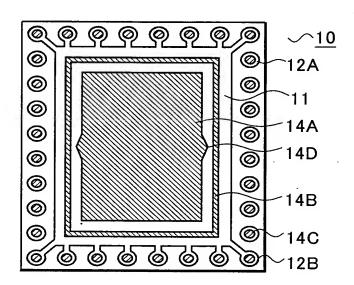


FIG.2B

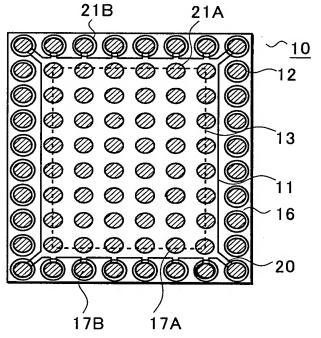
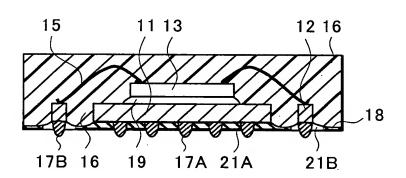


FIG.2C



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FIG.3A

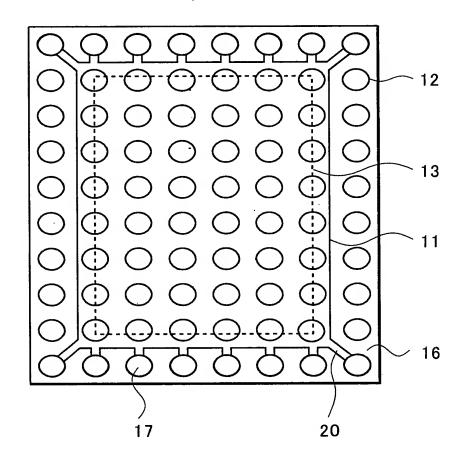
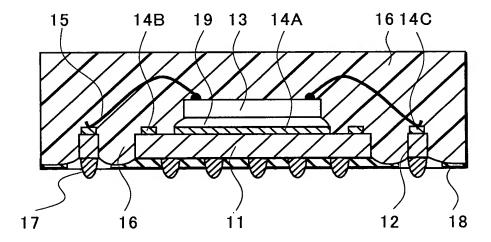


FIG.3B



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FIG.4A

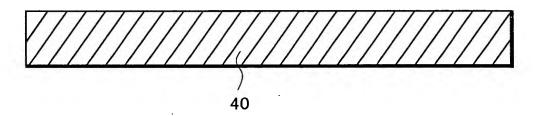


FIG.4B

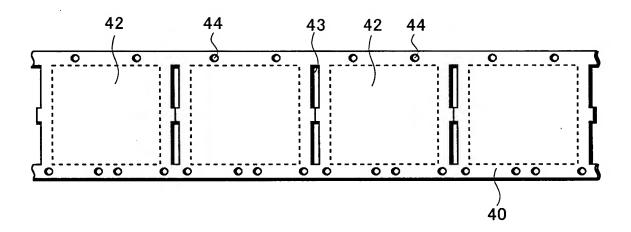


FIG.5

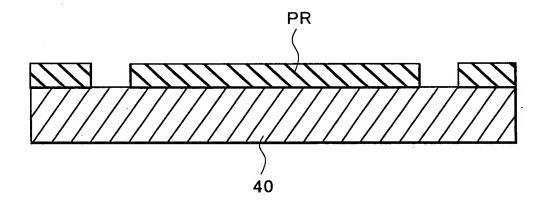


FIG.6A

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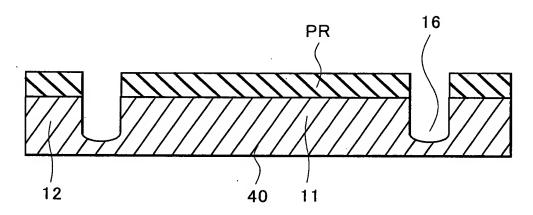


FIG.6B

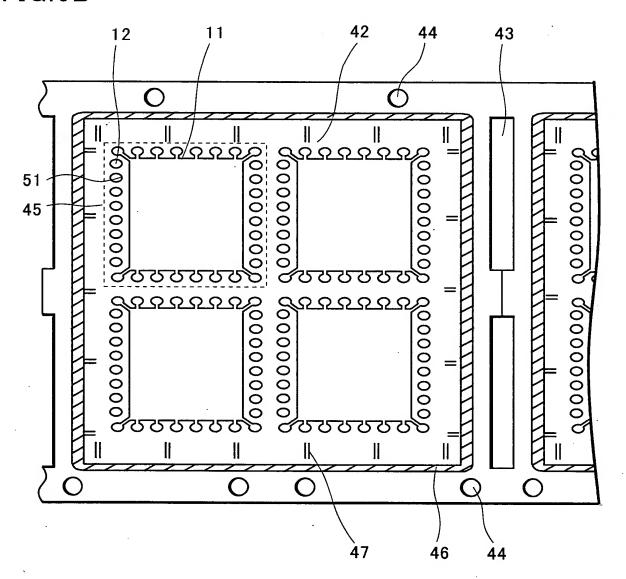
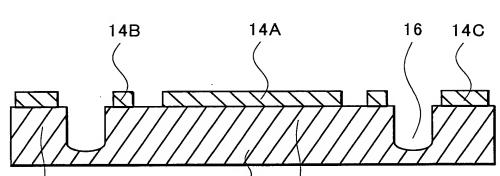


FIG.7A



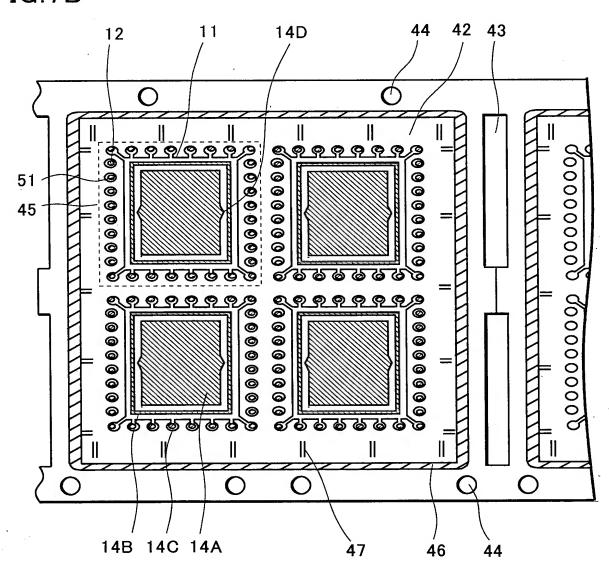
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FIG.7B

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SAME

FIG.8A

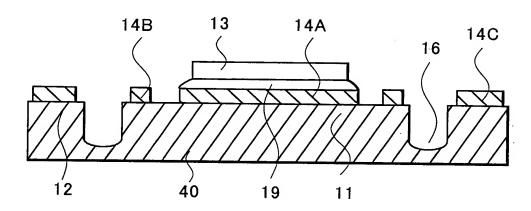
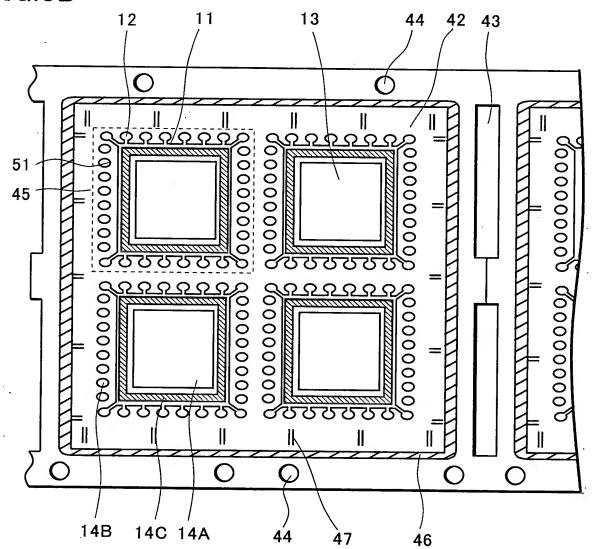


FIG.8B



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FIG.9A

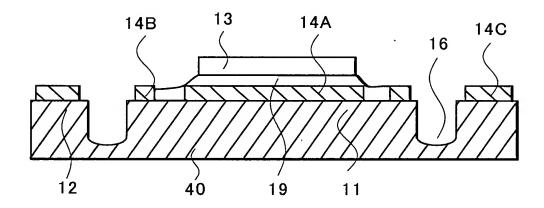
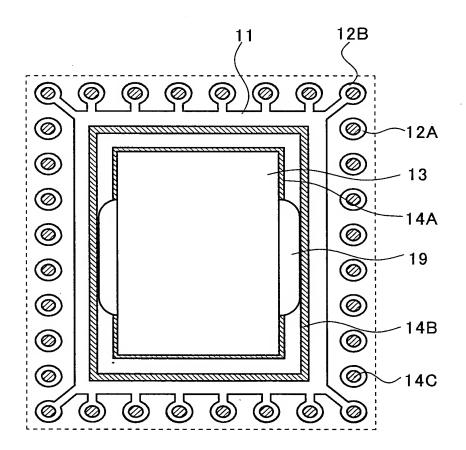


FIG.9B



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CIRCUIT DEVICE AND METHOD OF MANUFACTURING THE

SAME

FIG.10A



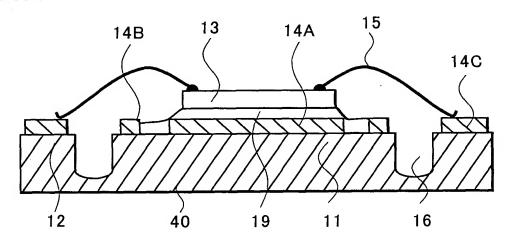
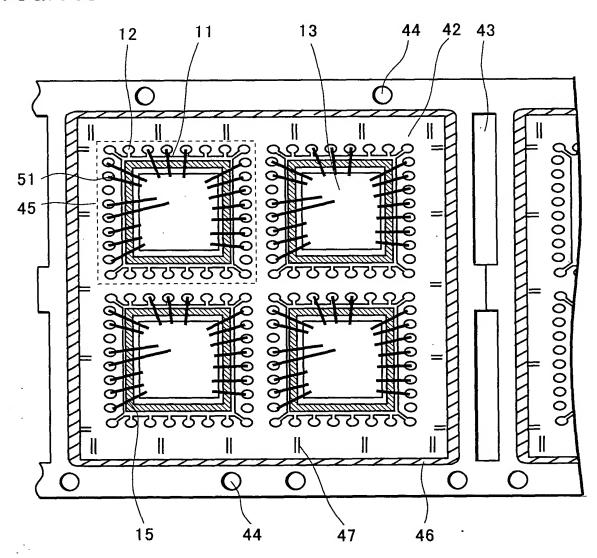


FIG.10B



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FIG.11A

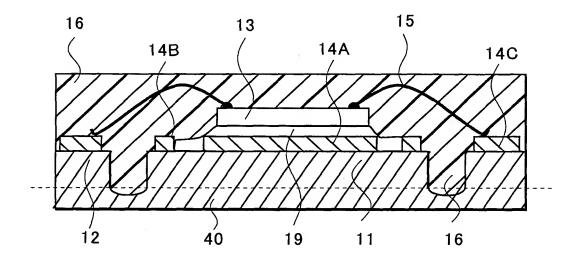
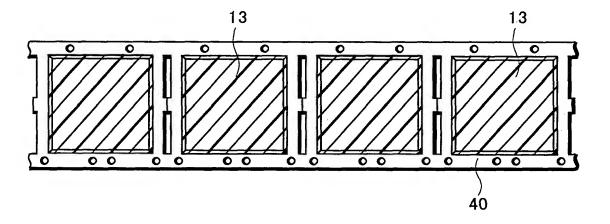


FIG.11B



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FIG.12A

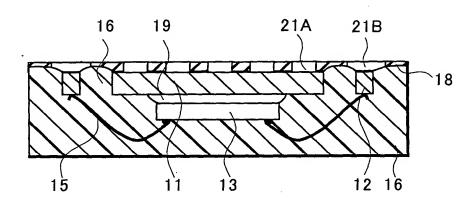


FIG.12B

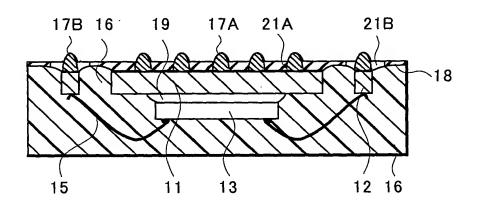
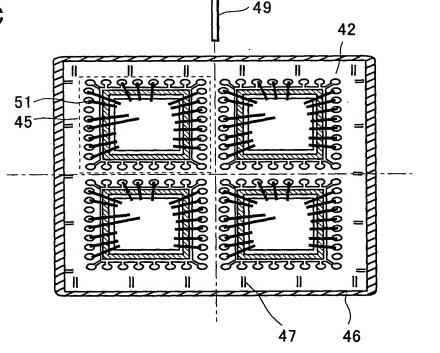


FIG.12C



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FIG.13

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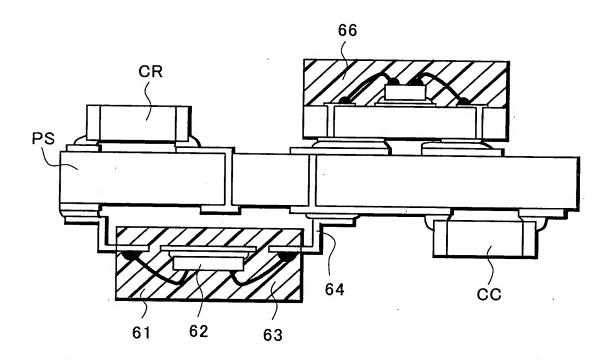


FIG.14

